



LIST OF PRIOR ART CITED BY APPLICANT  
(Use several sheets if necessary)

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U. S. PATENT DOCUMENTS

| * DOCUMENT NUMBER | DATE     | NAME             | CLASS* | SUB-CLASS* | FILING DATE IF APPRO. |
|-------------------|----------|------------------|--------|------------|-----------------------|
| 5725674           | 3/10/98  | Moustakas et al. | —      | —          |                       |
| 6051849           | 4/18/00  | Davis et al.     | —      | —          |                       |
| 5858862           | 1/12/99  | Westwater et al. | —      | —          |                       |
| 5962863           | 10/5/99  | Russell et al.   | —      | —          |                       |
| 5381753           | 1/17/95  | Okajima et al.   | —      | —          |                       |
| 6063246           | 5/16/00  | Wolfe et al.     | —      | —          |                       |
| 4886683           | 12/12/89 | Hoke et al.      | —      | —          |                       |
| 5334296           | 8/2/94   | Henkens et al.   | —      | —          |                       |
| 6033866           | 3/7/00   | Guo et al.       | —      | —          |                       |
| 5922183           | 7/13/99  | Rauh             | —      | —          |                       |

FOREIGN PATENT DOCUMENTS

\* Supply data if known.

| * DOCUMENT NUMBER | DATE    | COUNTRY | NAME                | CLASS* | SUB-CLASS* | PERTINENT SPTS DWG | PP SP |
|-------------------|---------|---------|---------------------|--------|------------|--------------------|-------|
| 2086121           | 3/27/90 | Japan   | Nishibe Toru et al. | —      | —          |                    |       |
| 11177134          | 7/2/99  | Japan   | Yamada Takeshi      | —      | —          |                    |       |

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Publication No. WO026422A1 for "HIGH PURITY GALLIUM FOR PREPARATION OF COMPOUND SEMICONDUCTOR, AND METHOD AND APPARATUS FOR PURIFYING THE SAME" by Yamamura et al., published on May 11, 2000;

Publication No. WO9965068A1 for "FABRICATION OF GALLIUM NITRIDE SEMICONDUCTOR LAYERS BY LATERAL GROWTH FROM TRENCH SIDEWALLS" by Zheleva et al., published on December 16, 1999;

Publication No. WO9944224A1 for "METHOD OF FABRICATING GALLIUM NITRIDE SEMICONDUCTOR LAYERS BY LATERAL OVERGROWTH THROUGH MASKS, AND GALLIUM NITRIDE SEMICONDUCTOR STRUCTURES FABRICATED THEREBY" by Davis et al., published on September 2, 1999;

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\* = Examiner: Please mark this space to indicate you have considered this reference.

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| *  | OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)  |
|----|---|
| Cb | J.L. Gole and J.D. Stout, W.L. Rauch and Z.L. Wang "DIRECT SYNTHESIS OF SILICON NANOWIRES, SILICA NANOSPHERES, AND WIRE-LIKE NANOSPHERE AGGLOMERATES", Applied Physics Letters, Vol. 76, Number 17, 24 April 2000, 2346-2348;   |
| i  | J. D. Holmes, K.P. Johnston, R.C. Doty, B. A. Korgel "CONTROL OF THICKNESS AND ORIENTATION OF SOLUTION-GROWN SILICON NANOWIRES", Science, Vol. 287, February 25, 2000, 1471-1473;   |
|    | P. Scheier, J. Marsen, M. Lonfat, W. Schneider, K. Sattler "GROWTH OF SILICON NANOSTRUCTURES ON GRAPHITE", Surface Science 458 (2000, 113-122; <i>no date</i>   |
|    | D.P. Yu, Z.G. Bai, Y. Ding, Q.L. Hang, H.Z. Zhang, J.J. Wang, Y.H. Zou, W. Qian, G.C. Xiong, H.T. Zhou, and S.Q. Feng "NANOSCALE SILICON WIRES SYNTHESIZED USING SIMPLE PHYSICAL EVAPORATION", Applied Physics Letters, Vol. 72, Number 26, June 29, 1998, 3458-3460; |



